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**OFFICIAL**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

*Garg et al.*

Appl. No. 09/188,708

Filed: November 10, 1998

For: **RISC Microprocessor Architecture  
Implementing Multiple Typed  
Register Sets**

Art Unit: 2783

Examiner: *To Be Assigned*

Atty. Docket: SP018.C4

**Correspondence Address Reminder** **RECEIVED**

Assistant Commissioner for Patents  
Washington, D.C. 20231

SEP 16 1999

GROUP 2700

Sir:

Applicants wish to remind the Office to direct all communications in reference to this application to:

Sterne, Kessler, Goldstein & Fox P.L.L.C.

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This reminder is submitted to avoid any confusion due to the fact that the Declaration filed in the application lists the former address of Applicants' representative, although the original Application Transmittal Form lists the above address.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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Date: April 26, 1999

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